



Docket No. 0756-1986

#26/E
3/14/3
Jules

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Yasuhiko TAKEMURA) Group Art Unit: 2814
Serial No. 09/342,235) Examiner: Ahmed N. Sefer
Filed: June 29, 1999)
For: SEMICONDUCTOR DEVICE)
HAVING AT LEAST FIRST AND)
SECOND THIN FILM)
TRANSISTORS)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on February 24, 2003.

Adele M. Stamper
Adele M. Stamper

AMENDMENT

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action dated October 23, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

13. (Amended) A semiconductor device comprising:
- a substrate having an insulating surface;
 - at least first and second semiconductor islands formed directly on said insulating surface wherein each of the semiconductor islands has a channel region and a pair of impurity regions;
 - a first and a second gate insulating film formed over said semiconductor island, respectively;

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